Search Notes



Application No.	Applicant(s)	
09/757,404	GUCCIONE ET AL.	
Examiner	Art Unit	
Mary C Hogan	2123	

SEARCHED				
Class	Subclass	Date	Examiner	
716	all	2/10/2005	мсн	
703	14,17,20	2/10/2005	мсн	

INTERFERENCE SEARCHED				
Class	Subclass	Date	Examiner	
703	14,17,20	2/10/2005	мсн	
716	all	2/10/2005	мсн	

SEARCH NOTES (INCLUDING SEARCH STRATEGY)			
	DATE	EXMR	
EAST: configuration, bitstream, object, event, queue, simulate, input, output, signal	2/10/2005	мсн	
Consulted with Sam Broda, PRI AU2123 and Leigh Garbowski, PRI AU2825	2/10/2005	мсн	
IEEE: 'configuration bitstream' <and> objects <and> state <and> input <and> output <and> signal <and> event</and></and></and></and></and></and>	2/10/2005	мсн	
IEEE: configuration bitstream and simulation fpga <and> (simulation <or> simulate) <and> objects <and> event</and></and></or></and>	2/10/2005	МСН	
IEEE and EAST: inventor name search, search on prior patents	2/10/2005	мсн	
Proquest: config and bitstream and event	2/10/2005	мсн	